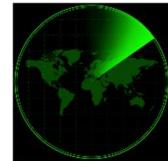


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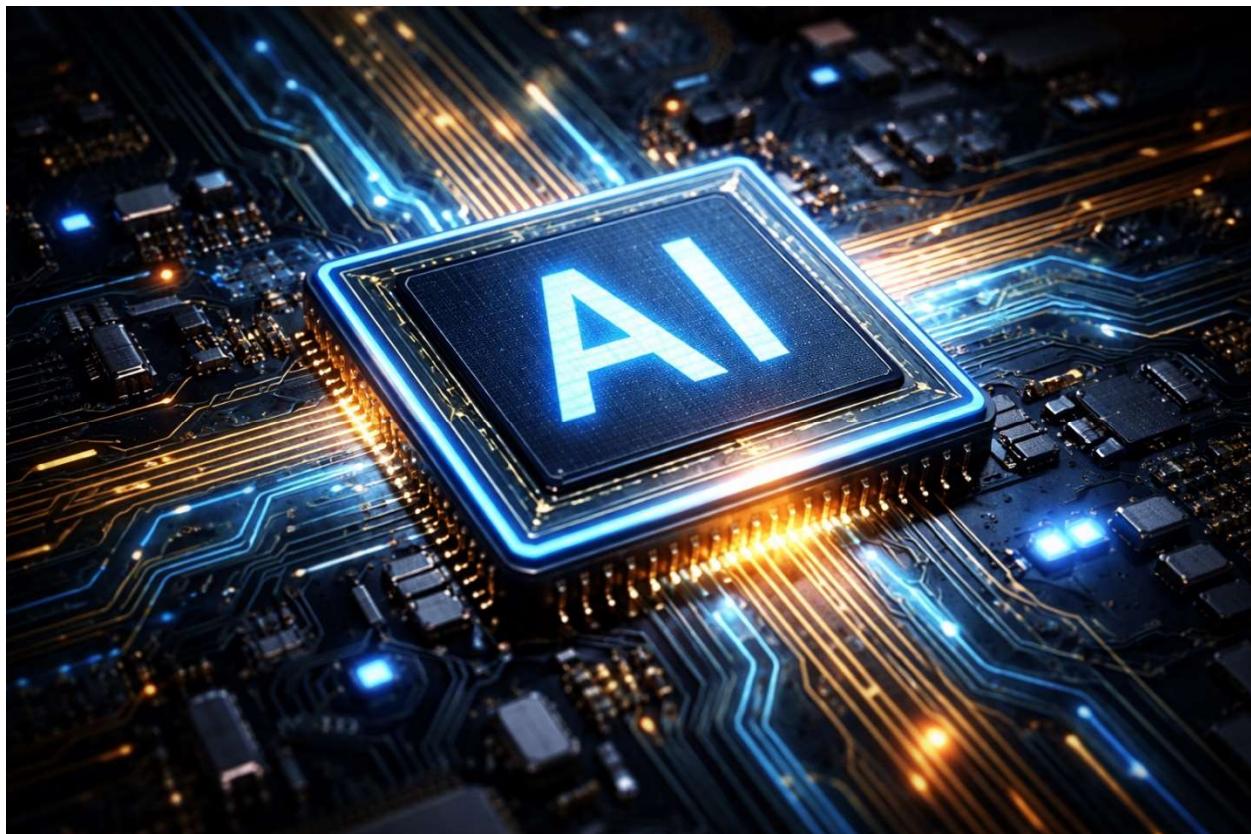


Google Cloud

Global ASIC TPU, CPU, & QPU Deployment Analysis

“Under the Radar” AIxPU Research Series

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